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PLEASE DELIVER TO EXAMINER STEVEN HO YIN LOKEDocket No. BUR920030102US1 (IBU-10-5802)Applicant(s): Reynolds et al

Serial No.	Filing Date	Examiner	Group Art Unit
<u>10/731,296</u>	<u>December 9, 2003</u>	<u>S. Loke</u>	<u>2811</u>

Invention:

FPGA BLOCKS WITH ADJUSTABLE POROSITY PASS THRU

I hereby certify that this AMENDMENT AND REPLACEMENT SHEET 1 OF DRAWINGS is being transmitted via facsimile to the United States Patent and Trademark Office.

Fax. No. 703-872-9306 on May 27, 2005 14
(Date) (No. of pages)

TO: Examiner Loke

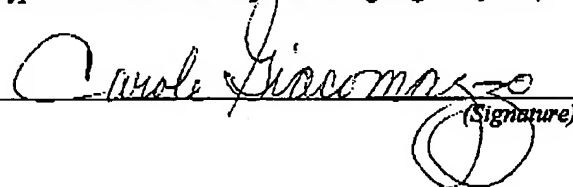
Attached is an amendment with replacement sheet 1 of the drawings being filed in response to your Office action of February 28, 2005.

It is not believed that any fees are required. However, the Commissioner is hereby authorized to charge payment of fees associated with this communication, or credit any overpayment, to Deposit Account No. 09-0457.

FROM: James A. Lucas
CUSTOMER NO. 26681

Carole Giacomazzo

(Typed or Printed Name of Person Signing Certificate)


(Signature)

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PATENT

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re application of Reynolds et al)	Examiner: Steven Ho Yin Loke
)	
Serial No. 10/731,296)	Art Unit 2811
)	
Filed: December 9, 2003)	Confirmation No. 3650
)	
For: FPGA BLOCKS WITH ADJUSTABLE)	
POROSITY PASS THRU)	

Docket No. BUR920030102US1 (IBU-10-5802)

AMENDMENT

Mail Stop Amendment
Commissioner for Patents
P. O. Box 1450
Alexandria, VA 22313-1450

Dear Sir:

Responsive to the Examiner's Office Action of February 28, 2005, it is respectfully requested that the drawings, specification and claims be amended as shown on the attached sheets. Specifically, applicants are responding to the Examiner's objection to the disclosure and the rejection of claims based on indefiniteness under 35 USC 112 (2nd par.), and the anticipation of claims 1-3 under 35 USC 102(b)